## In re PATENT APPLICATION of Applicant(s) : Toru MORI Appln. No. : To Be Assigned **INFORMATION** Filed : December 31, 2003 For : MANUFACTURING METHOD FOR

SOI SEMICONDUCTOR DEVICE, AND SOI SEMICONDUCTOR DEVICE

: FUJI 139 Atty. Dkt.

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

This is an information disclosure statement submitted in compliance with the timing requirements of 37 C.F.R. §1.97(b)(1).

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: To Be Assigned Examiner: To Be Assigned

> **DISCLOSURE STATEMENT**

Attached is a copy of Japanese patent publication. The relevance of the publication can be gleaned from the attached English-language Abstract, and from page 2 of the specification, where this document is discussed. The publication is listed on the attached Form PTO-1449.

Since this Information Disclosure Statement is being filed with the application, no certification or fee is required, and the requirements of 37 C.F.R. §§1.97 and 1.98 are deemed to be fully met as to the document submitted. Consideration of the submitted document is respectfully requested.

Respectfully submitted,

December 31, 2003

Date

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FORM PTO-1449 INFORMATION DISCLOSURE STATEMENT				Atty Docket Application No.				
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				Applicant Toru MORI				
				Filing Date	1 .	Group Unit		
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U.S. PATENT DOCUMENTS								
Examiner Initial		Document Number	Date	Name	Class	Sub- Class	Filing Date	
	AA							
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FOREIGN PATENT DOCUMENTS								
		Document Number	Date	Country	Class	Sub- Class	Trans- lation	
	AL	08-330250	12/13/96	Japan			Abstract	
	AM							
	AN					-		
	AO							
OTHER (Including Author, Title, Date, Pertinent Pages, etc.)								
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through								
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